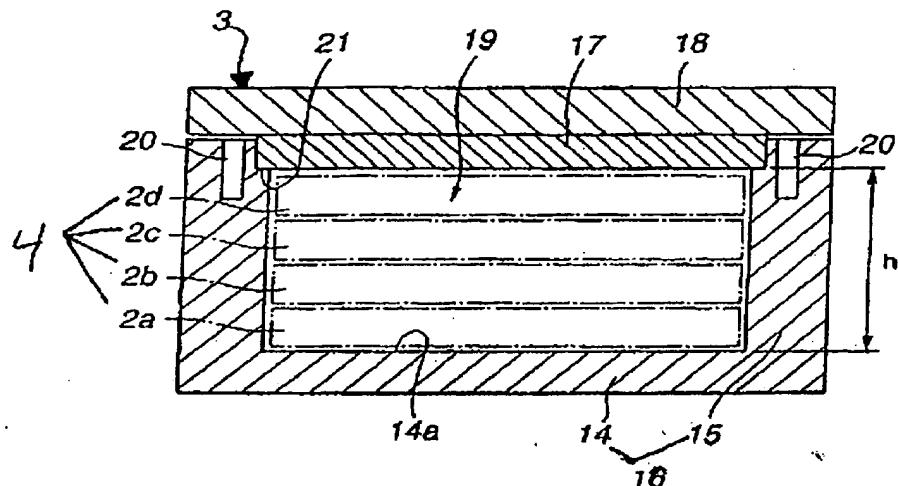
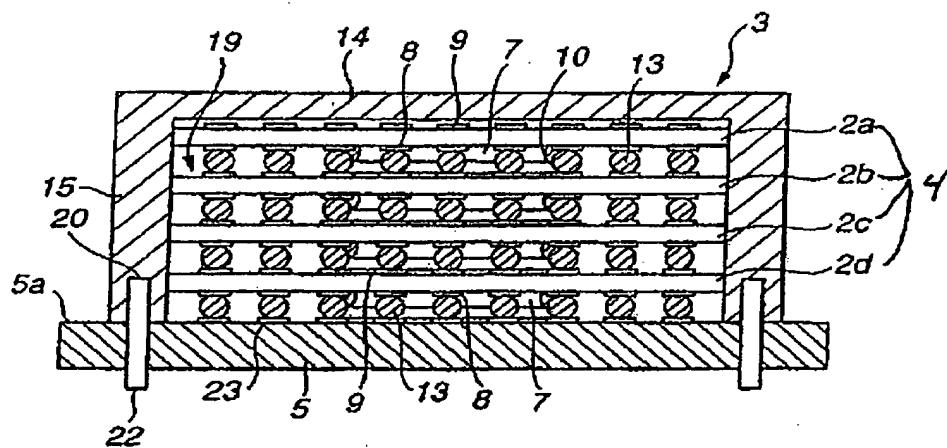


ASSEMBLY JIG AND MANUFACTURING METHOD OF MULTILAYER SEMICONDUCTOR DEVICE  
REPLACEMENT DRAWINGS Inventor: Yoshiyuki Yanagisawa et al.

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**FIG.3****FIG.4**